

EAST Search History

10/721,646

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L2	6	compar\$3 and (first MISR same second MISR)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/04/09 10:22
L3	4558	boundary scan	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/04/09 10:24
L4	29358	714/732 or 714/728 or 714/738 or 714/739 or 714/724 or 714/725 or 714/726 or 714/727 or 714/733 or 714/734 or 714/718 or 365/201 or 716/4 or 703/? or 714/?	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/04/09 10:26
L5	2247	signature analyzer\$1 or signature analysis	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/04/09 10:28
L6	27	(first MISR or second MISR)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/04/09 10:27
L7	4	I5 and I6	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/04/09 10:27
L8	4498	linear feedback shift register or (LFSR)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/04/09 10:27

EAST Search History

L10	11	I6 and I8	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/04/09 10:50
L12	8	I10 and I4	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/04/09 10:28
L13	3950	signature analyzer\$1 or signature analysis or signature value	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/04/09 10:28
L14	7	I12 and I13	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/04/09 10:31
S1	0	(microchip AND test computer AND (LFSR) AND (MISR) AND shift register AND "OR" AND built-in self test AND signature analyzer AND sequential logic circuit AND combinatorial logic circuit AND seed signature value).clm.	US-PGPUB; USPAT; USOCR	ADJ	ON	2006/11/30 09:04
S2	411	signature analyzer\$1	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/04/09 10:26
S3	893	multiple input shif register\$1 or "MISR"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/11/29 12:10